

10/057 821

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,791,119 B2
DATED : September 14, 2004
INVENTOR(S) : Slater, Jr. et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [56], **References Cited, OTHER DOCUMENTS**, should include:

-- U.S. Application Serial No. 60/411,980, filed 09/19/02, *Phosphor-Coated Light Emitting Diodes Including Tapered Sidewalls, and Fabrication Methods*

U.S. Application Serial No. 10/003,331, filed 10/31/01, *Low Temperature Formation of Backside Ohmic Contacts for Vertical Devices*

U.S. Application Serial No. 60/294,445, filed May 30, 2001, *Multi-Quantum Well Light Emitting Diode Structure*

U.S. Application Serial No. 60/294,378, filed May 30, 2001, *Light Emitting Diode Structure With Multi-Quantum Well and Superlattice Structure*

U.S. Application Serial No. 60/294,308, filed May 30, 2001, *Light Emitting Diode Structure With Superlattice Structure*

U.S. Application Serial No. 09/787,189, filed 03/15/01, *Low Temperature Formation of Backside Ohmic Contacts for Vertical Devices* --

Column 29.

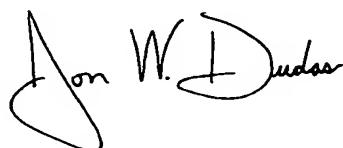
Line 3, should read -- **90**. A light emitting diode according to claim **76** further --

Line 27, should read -- **96**. A light emitting diode according to claim **35** further --

Line 34, should read -- **98**. A light emitting diode according to claim **3** wherein --

Signed and Sealed this

Fifth Day of April, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office